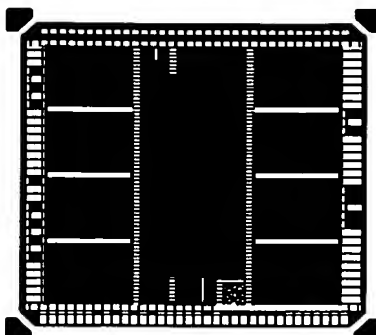




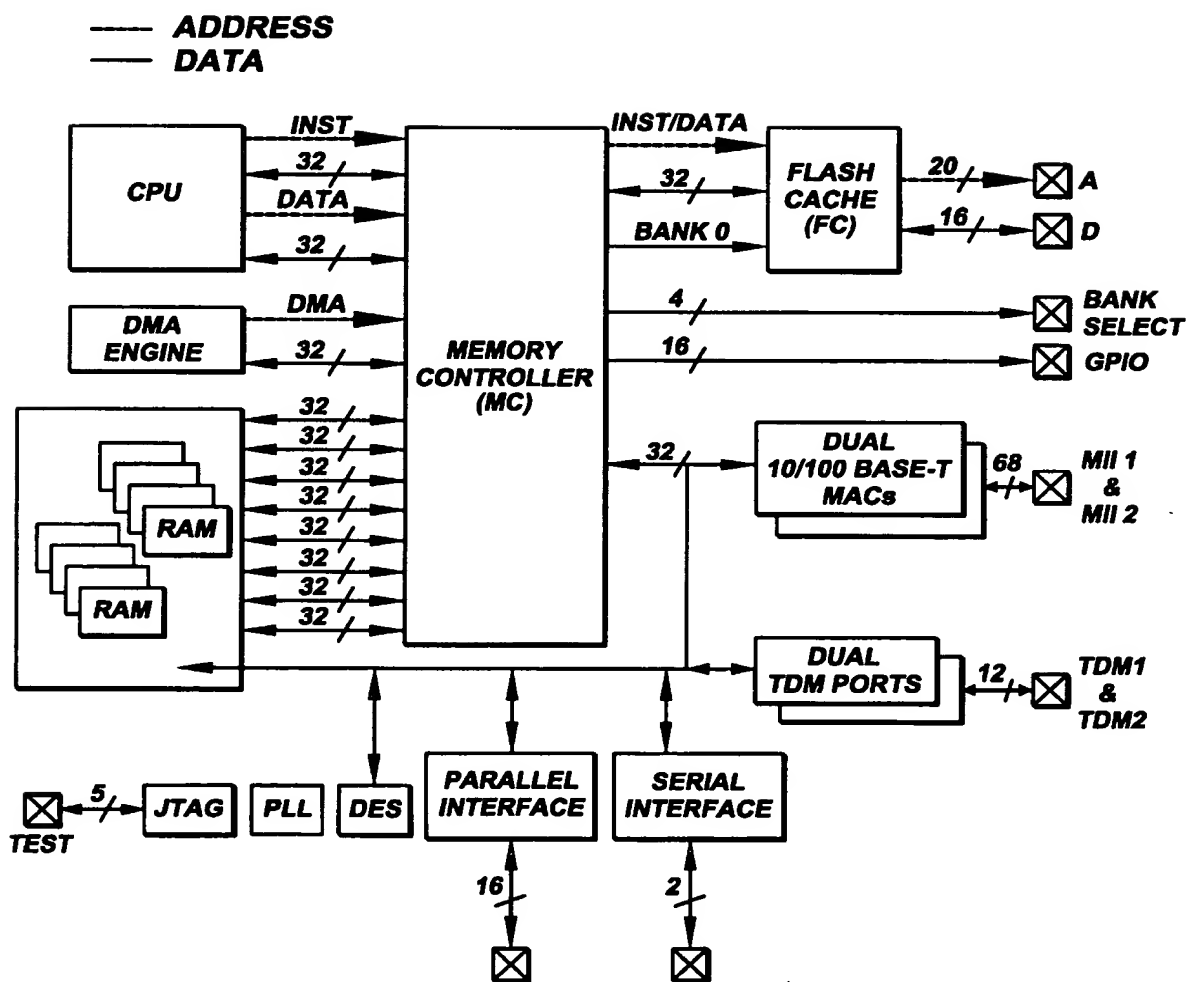
Serial No.: 09/662,077  
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**FIG.1**



**FIG.2**



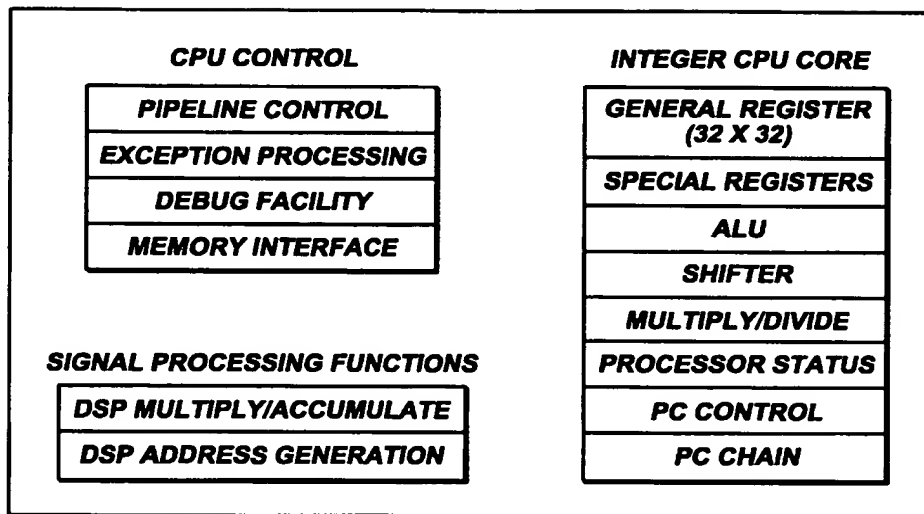
BEST AVAILABLE COPY



Serial No.: 09/662,077  
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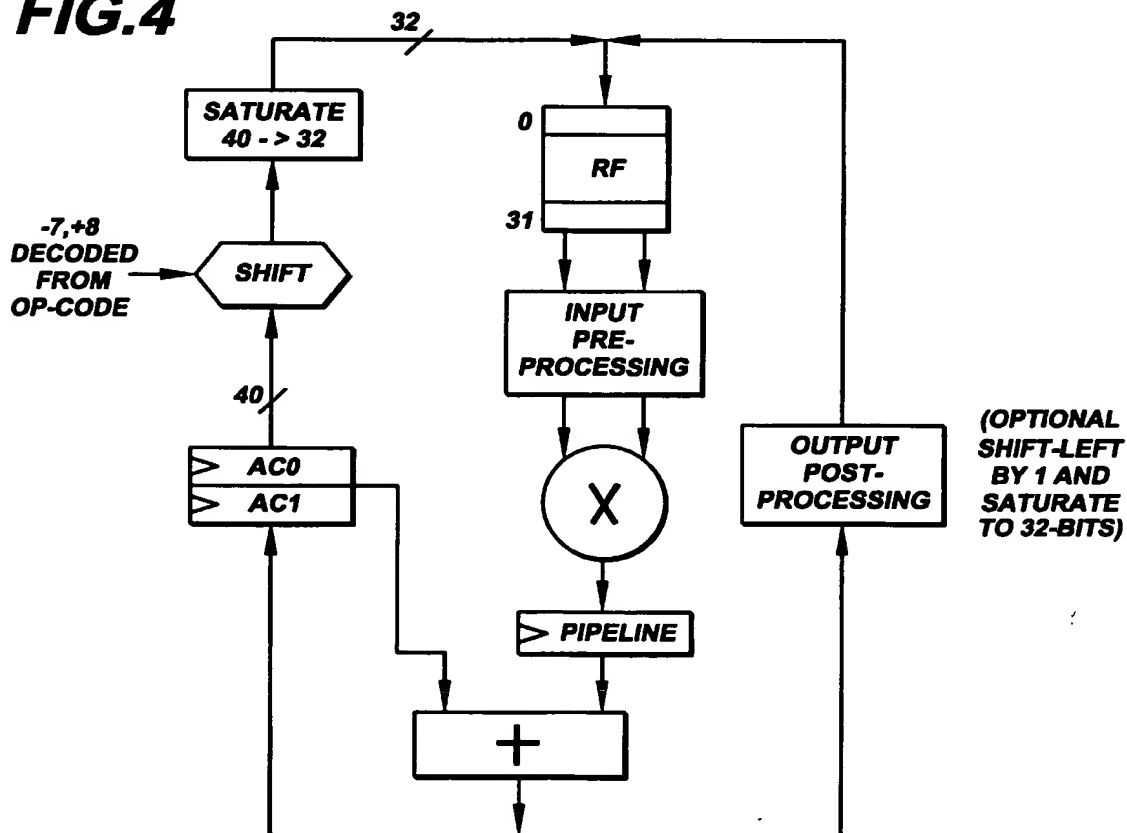
**FIG.3**

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**PIPELINE: IF RF EXE MEM WB**  
IF: INSTRUCTION FETCH, RF: REGISTER FETCH, EXE: EXECUTE, MEM: MEMORY ACCESS, WB: WRITE BACK

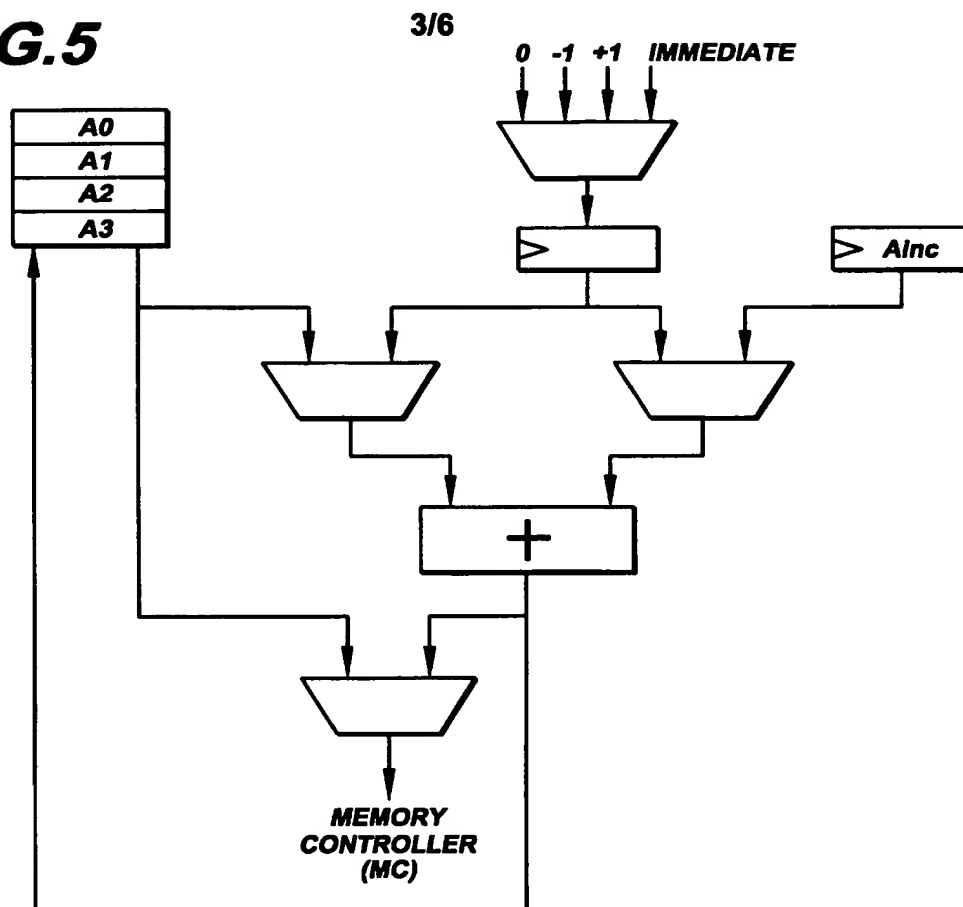
**FIG.4**



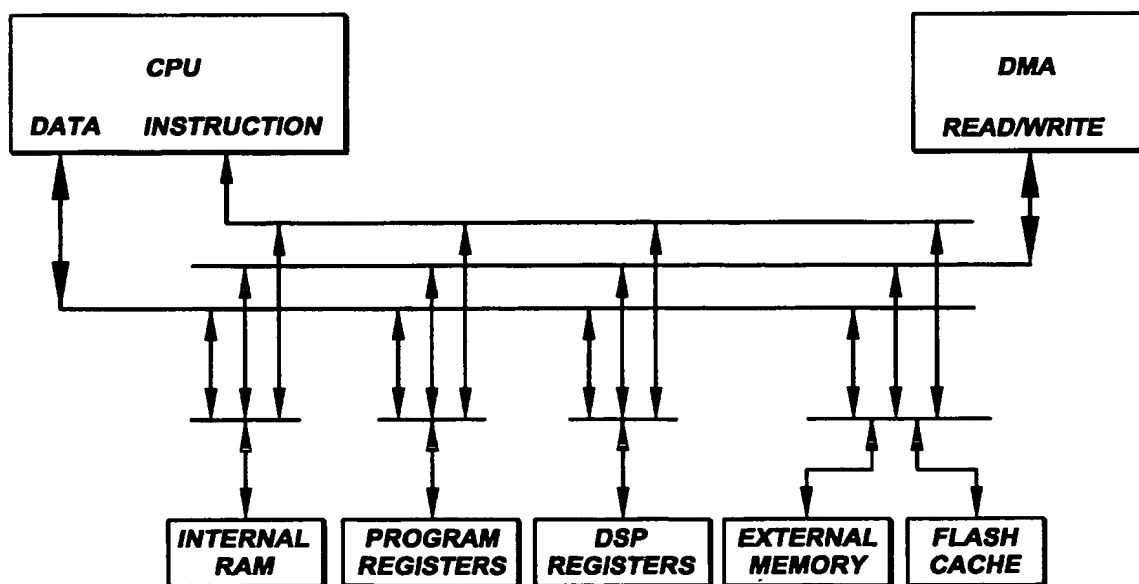


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**FIG.5**



**FIG.6**



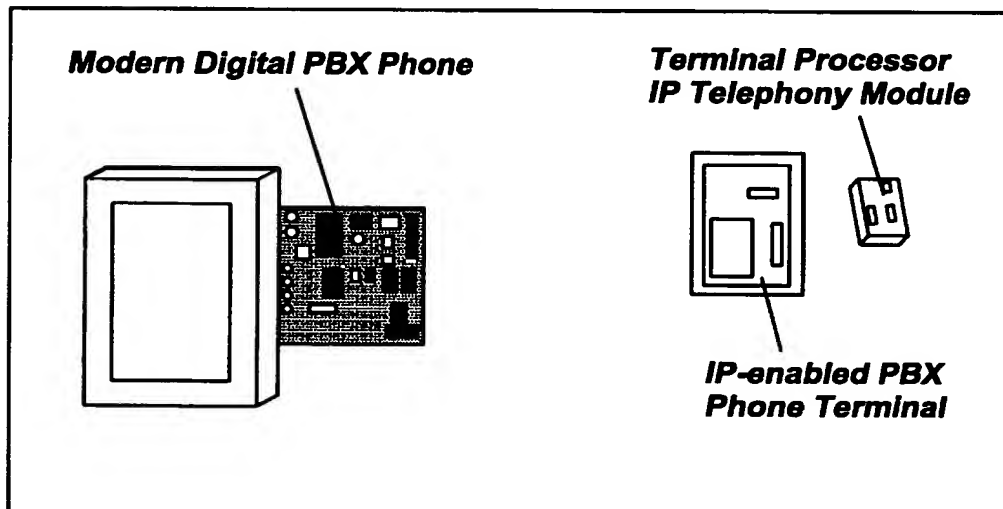




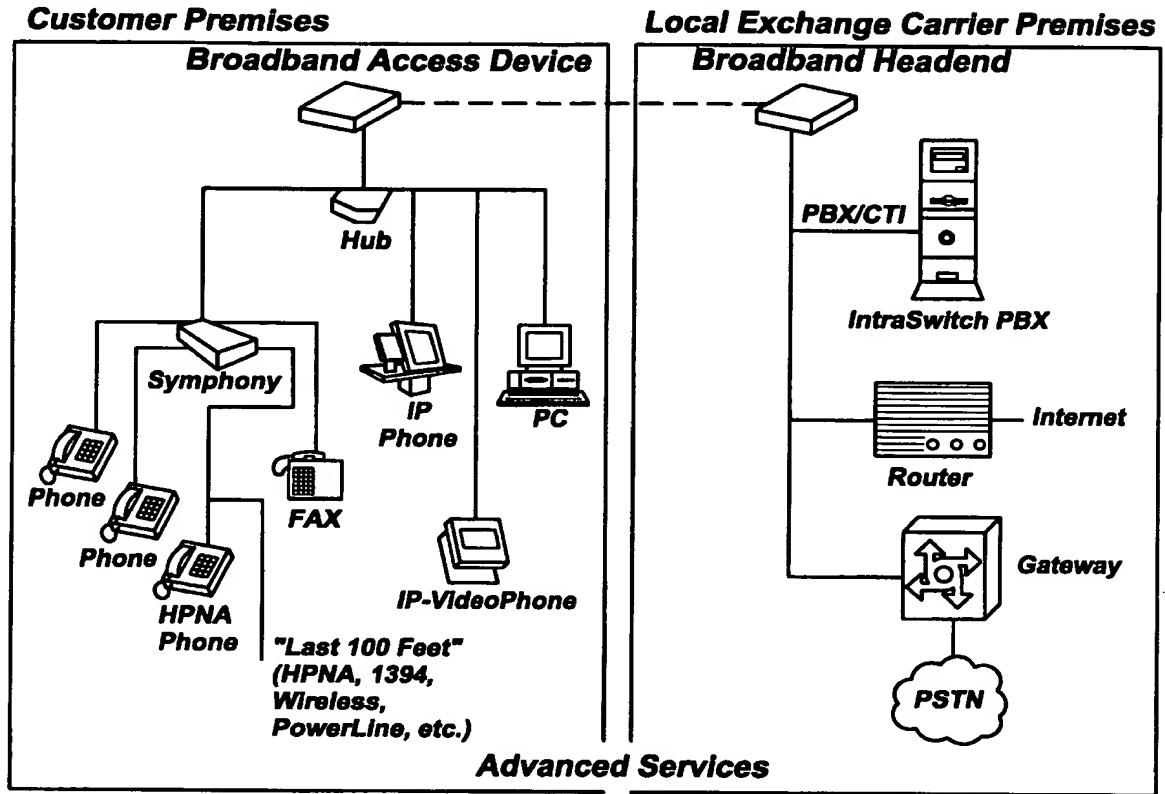
## FIG.9

- **Implementation of the POSIX operating system**
- **Full support for:**
  - **Threads (single process, multiple threads)**
  - **Scheduling (two algorithms – FIFO and round robin)**
  - **Semaphores**
  - **Mutexes**
  - **Condition Variables**
  - **Message Queues**
  - **Signal and Timers**

## FIG.10



**FIG.11**



**FIG.12**

